

## PATENT ABSTRACTS OF JAPAN

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(22)Date of filing : 05.10.1982 (72)Inventor : KITAGAWA TAKASHI

#### (54) BINARY CODING SYSTEM

### (57)Abstract:

**PURPOSE:** To obtain an accurate binary coded signal by setting the threshold level between the white and black peak levels most approximate to the analog picture signal to be binary coded.

**CONSTITUTION:** The analog picture signal supplied from a terminal 60 is converted 64 into a digital signal and then written to memories 67 and 68 when the white and black peaks are detected by a black peak detecting circuit 63 respectively. Then the black and white peak levels are read out of the memories 68 and 67 when the white and black peaks are detected out of a delayed 61 analog picture signal by white and black peak detecting circuits 65 and 66 respectively. These peak levels are held until the next white and black peaks are detected. The white and black peak levels read out of the memories 67 and 68 are added 69 together and then converted into analog signals by a D/A converting circuit 70 with a 1/2 conversion factor, for example. In a comparator 71, the delayed 61 picture signal is binary coded with the output of the circuit 70 defined as the threshold level.

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#### 154. BINARY CODING SYSTEM

### LEGAL STATUS

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examiner's decision of rejection or application converted to a registration] (see art. 63, para. 1, letter b, of the TRIPS Agreement).

[Date of final disposal for application] 67 when the white and black

[Patent number] is derived from a delayed 61 and long duration signal by three [Date of registration] and three long circuits 63 and 61 respectively. These peak

[Date of registration] events are held until the next visible and black phase are selected. The [Number of appeal against examiner's decision of rejection] of 3 are

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